# 100 MHz, 100 MS/s, 14-Bit Digitizer

# NI 5122

- 2 channels simultaneously sampled at 14-bit resolution
- 100 MS/s real-time and 2.0 GS/s random interleaved sampling
- 100 MHz bandwidth
- 50 Ω or 1 MΩ input impedance, software-selectable
- 200 mV to 20 V input range
- 75 dBc SFDR and 62 dB SINAD
- Memory options from 8 to 512 MB per channel
- Edge, window, hysteresis, video, and digital triggering with 100 ps timestamping

#### Models

- NI PCI-5122
- NI PXI-5122
- NI PXIe-5122

#### **Operating Systems**

Windows Vista/XP/2000

#### **Recommended Software**

- LabVIEW
- LabWindows™/CVI
- Measurement Studio for Visual Studio

#### **Other Compatible Software**

- Visual Basic
- C/C++
- .NET

#### **Application Software (included)**

Spectral Measurements Toolkit
 (32 and 256 MB/channel models only)

#### **Driver Software (included)**

• NI-SCOPE



# **Overview**

National Instruments 5122 high-speed digitizers feature two 100 MS/s simultaneously sampled input channels with 14-bit resolution, 100 MHz bandwidth, and up to 512 MB of memory per channel in a compact, 3U PXI Express, PXI, or PCI device. With its high sampling rate and low-distortion front end, an NI 5122 is ideal for a wide range of applications in automotive, communications, scientific research, military/ aerospace, and consumer electronics. Using the National Instruments Synchronization and Memory Core (SMC) architecture, you can easily synchronize to other analog and digital instruments to develop high-channel-count or mixed-signal test systems.

# **Analog Input Performance**

NI 5122 digitizers use 14-bit analog-to-digital converters (ADCs), low-noise variable-gain amplifiers, and a low-jitter 100 MHz timebase to deliver a 75 dBc spurious-free dynamic range and a 62 dB signal-tonoise ratio. The 14-bit data converters have 64 times the resolution of traditional 8-bit instruments, providing more accurate time- and frequency-domain measurements.

Software-selectable 50  $\Omega$  or 1 M $\Omega$  input impedance, input ranges from 200 mV\_pp to 20 V\_pp, seven trigger modes, and antialias and noise filters make NI 5122 digitizers versatile enough to meet the most demanding application requirements. The programmable DC offset feature maximizes the use of the entire 14 bits of vertical range. Onboard self-calibration also ensures measurement stability over the entire operating temperature range of 0 to 55 °C.



Figure 1. Graph of Dynamic Performance (FFT)

# **Deep Onboard Acquisition Memory**

An NI 5122, based on the SMC architecture, comes with high-speed memory options from 8 to 512 MB per channel (4 to 256 million 14-bit samples per channel). An NI 5122 can acquire more than 1 million triggered waveforms without software intervention in multiple-record acquisition mode for applications such as RADAR, ultrasound, and event detection, which require short trigger rearm times. In addition, you can timestamp each triggered event with 100 ps resolution in both singleshot and multiple-record acquisition modes. An NI 5122 also can stream data continuously from onboard memory to host memory for longer acquisitions and streaming to disk.





Figure 2. Hardware Block Diagram

## Data Streaming up to 400 MB/s

Because of the PCI and PCI Express buses used in PXI, an NI 5122 can continuously stream data to the host computer at rates up to 110 MB/s using PCI/PXI or 400 MB/s using PXI Express. At 400 MB/s, an NI 5122 is streaming data on both channels at its maximum data rate. The dedicated per-slot bandwidth available in PXI Express enables multi-module systems to achieve higher aggregate data rates. High-speed data record and playback applications are possible using the host computer's memory or high-end storage solutions such as RAID. Using an 8 x 500 GB drive RAID system with a capacity of 4 TB, you can capture data at 400 MB/s for more than 2.5 hours. Areas that benefit from this capability include RF/IF data streaming in signal intelligence, data record and playback, and scientific applications.

# Triggering

NI 5122 digitizers have three trigger sources – analog, digital, and software control. Compare the input signal on either channel or the external trigger channel to one or two thresholds for edge, hysteresis, or window trigger detection. You can also use line-selectable video triggering for NTSC, PAL, or SECAM broadcast standards. Drive and receive digital triggers to and from the PXI trigger bus or the external 9-pin AUX connector. You can specify the number of samples to acquire before and after a trigger event occurs. These pretrigger and posttrigger settings also apply when the module is used in multiple-record mode.

# **Timing and Synchronization**

An advanced 100 MHz clock generator produces the low-jitter, low-phaseskew clock for the precise clocking and stable synchronization necessary for high-speed, high-resolution digitizers. You can also use an external clock source, such as the NI PXI-5404 100 MHz frequency generator, for applications that require very specific sample frequencies or you can clock directly from the device under test. Synchronize multiple instruments using the PXI backplane 10 MHz reference clock or an external reference ranging from 1 to 20 MHz in 1 MHz increments. Because an NI 5122 is built on the SMC architecture, you can synchronize two or more digitizers for high-channel-count applications and build mixed-signal test systems using NI PXI-5421 arbitrary waveform generators and NI PXI-655x digital waveform generator/analyzers.

## Software

Every National Instruments high-speed digitizer comes with the IVI-compliant NI-SCOPE driver, which is fully compatible with NI LabVIEW, LabWindows/CVI, and Measurement Studio for Visual Studio 6.0 and .NET. NI-SCOPE includes more than 50 built-in measurement and analysis functions and an interactive SCOPE Soft Front Panel. The NI Spectral Measurements Toolkit gives you sophisticated frequencydomain measurements, such as power in-band, multiple peak search, and 3D spectrogram, for applications in communications, signal intelligence, and avionics.



Figure 3. Spectral Measurements Toolkit 3D Spectrogram

### **Ordering Information**

| NI PCI-5122  |            |
|--|------------|
| 8 MB/channel   | 778758-01  |
| 32 MB/channel  | 778758-02  |
| 256 MB/channel   | 778758-03  |
| NI PXI-5122  |            |
| 8 MB/channel   | 778756-01  |
| 32 MB/channel  | 778756-02  |
| 256 MB/channel   | 778756-03  |
| 512 MB/channel   | 778756-04  |
| NI PXIe-5122   |            |
| 8 MB/channel   | 779967-01  |
| 64 MB/channel  | 779967-02  |
| 256 MB/channel   | 779967-03  |
| Includes NI 5122 device, NI-SCOPE, and Scope Soft Front Panel. | The 32 and |

256 MB/channel models also include the NI Spectral Measurements Toolkit.

## **Specifications**

These specifications are valid for 0 to  $55^{\circ}$  C for PXI, and 0 to  $45^{\circ}$  C for PCI, unless otherwise stated.

### **Acquisition System**

| Number of channels  | 2 simultaneously sampled  |
|---|---|
| Vertical resolution   | 14 bits   |
| Bandwidth <sup>1</sup> (-3 dB)  | 100 MHz   |
| Bandwidth limit filters   |   |
| (software selectable)   | 20 MHz noise (2-pole Bessel)  |
|   | 40 MHz antialias  |
|   | (-6 dB, 6-pole Chebyshev)   |
| Maximum sample rate   | 100 MS/s real-time, 2 GS/s  |
|   | random interleaved sampling   |
| Onboard sample memory   | 8 to 512 MB per channel   |
|   | (4 to 256 million samples)  |
| Pretrigger and posttrigger data points <sup>2</sup>   | 0 to 100% of full record length   |
| Input impedance   | 50 $\Omega$ and 1 M $\Omega$ II 27 pF(± 2 pF),  |
|   | software selectable   |
|   |   |
| Maximum number of records for   |   |
| Maximum number of records for multiple-record acquisition <sup>3</sup>  | 32,768 for 8 MB/ch, 100,000 for   |
| Maximum number of records for<br>multiple-record acquisition <sup>3</sup>   | 32,768 for 8 MB/ch, 100,000 for all other memory options  |
| Maximum number of records for<br>multiple-record acquisition <sup>3</sup><br>Full scale input range   | 32,768 for 8 MB/ch, 100,000 for all other memory options 50 $\Omega$ : 200 mV, 400 mV, 1 V, 2 V,  |
| Maximum number of records for<br>multiple-record acquisition <sup>3</sup><br>Full scale input range   | 32,768 for 8 MB/ch, 100,000 for all other memory options 50 $\Omega$ : 200 mV, 400 mV, 1 V, 2 V, 4 V, 10 V  |
| Maximum number of records for<br>multiple-record acquisition <sup>3</sup><br>Full scale input range   | 32,768 for 8 MB/ch, 100,000 for<br>all other memory options<br>50 Ω: 200 mV, 400 mV, 1 V, 2 V,<br>4 V, 10 V<br>1 MΩ: 200 mV, 400 mV, 1 V, 2 V,  |
| Maximum number of records for<br>multiple-record acquisition <sup>3</sup><br>Full scale input range   | 32,768 for 8 MB/ch, 100,000 for<br>all other memory options<br>50 Ω: 200 mV, 400 mV, 1 V, 2 V,<br>4 V, 10 V<br>1 MΩ: 200 mV, 400 mV, 1 V, 2 V,<br>4 V, 10 V, 20 V   |
| Maximum number of records for<br>multiple-record acquisition <sup>3</sup><br>Full scale input range<br>Vertical offset ranges   | 32,768 for 8 MB/ch, 100,000 for all other memory options 50 $\Omega$ : 200 mV, 400 mV, 1 V, 2 V, 4 V, 10 V 1 M $\Omega$ : 200 mV, 400 mV, 1 V, 2 V, 4 V, 10 V, 20 V ±50% of full scale input range  |
| <ul> <li>Maximum number of records for<br/>multiple-record acquisition<sup>3</sup></li> <li>Full scale input range</li> <li>Vertical offset ranges</li> <li>Maximum input overload</li> </ul>   | 32,768 for 8 MB/ch, 100,000 for<br>all other memory options<br>50 Ω: 200 mV, 400 mV, 1 V, 2 V,<br>4 V, 10 V<br>1 MΩ: 200 mV, 400 mV, 1 V, 2 V,<br>4 V, 10 V, 20 V<br>±50% of full scale input range<br>50 Ω: 7 V <sub>rms</sub> with peaks ≤10 V,   |
| <ul> <li>Maximum number of records for<br/>multiple-record acquisition<sup>3</sup></li> <li>Full scale input range</li> <li>Vertical offset ranges</li> <li>Maximum input overload</li> </ul>   | 32,768 for 8 MB/ch, 100,000 for<br>all other memory options<br>50 $\Omega$ : 200 mV, 400 mV, 1 V, 2 V,<br>4 V, 10 V<br>1 M $\Omega$ : 200 mV, 400 mV, 1 V, 2 V,<br>4 V, 10 V, 20 V<br>±50% of full scale input range<br>50 $\Omega$ : 7 V <sub>rms</sub> with peaks ≤10 V,<br>1 M $\Omega$ : peaks ≤42 V  |
| <ul> <li>Maximum number of records for<br/>multiple-record acquisition<sup>3</sup></li> <li>Full scale input range</li> <li>Vertical offset ranges</li> <li>Maximum input overload</li> <li>Input coupling</li> </ul>                             | 32,768 for 8 MB/ch, 100,000 for<br>all other memory options<br>50 $\Omega$ : 200 mV, 400 mV, 1 V, 2 V,<br>4 V, 10 V<br>1 M $\Omega$ : 200 mV, 400 mV, 1 V, 2 V,<br>4 V, 10 V, 20 V<br>±50% of full scale input range<br>50 $\Omega$ : 7 V <sub>ms</sub> with peaks ≤10 V,<br>1 M $\Omega$ : peaks ≤42 V<br>AC, DC, GND (AC coupling                           |
| <ul> <li>Maximum number of records for<br/>multiple-record acquisition<sup>3</sup></li> <li>Full scale input range</li> <li>Vertical offset ranges</li> <li>Maximum input overload</li> <li>Input coupling</li> </ul>                             | 32,768 for 8 MB/ch, 100,000 for<br>all other memory options<br>50 $\Omega$ : 200 mV, 400 mV, 1 V, 2 V,<br>4 V, 10 V<br>1 M $\Omega$ : 200 mV, 400 mV, 1 V, 2 V,<br>4 V, 10 V, 20 V<br>±50% of full scale input range<br>50 $\Omega$ : 7 V <sub>rms</sub> with peaks ≤10 V,<br>1 M $\Omega$ : peaks ≤42 V<br>AC, DC, GND (AC coupling<br>on 1 M $\Omega$ only) |
| <ul> <li>Maximum number of records for<br/>multiple-record acquisition<sup>3</sup></li> <li>Full scale input range</li> <li>Vertical offset ranges</li> <li>Maximum input overload</li> <li>Input coupling</li> <li>AC coupling cutoff</li> </ul> | 32,768 for 8 MB/ch, 100,000 for<br>all other memory options<br>50 $\Omega$ : 200 mV, 400 mV, 1 V, 2 V,<br>4 V, 10 V<br>1 M $\Omega$ : 200 mV, 400 mV, 1 V, 2 V,<br>4 V, 10 V, 20 V<br>±50% of full scale input range<br>50 $\Omega$ : 7 V <sub>rms</sub> with peaks ≤10 V,<br>1 M $\Omega$ : peaks ≤42 V<br>AC, DC, GND (AC coupling<br>on 1 M $\Omega$ only) |

#### Accessories

| Recommended PXI switch                 |            |
|--|------------|
| NI PXI-2593                            | 778793-01  |
| Switchable 1/10x probe                 |            |
| SP200B                                 | 763391-01  |
| 9-pin DIN to BNC for AUX I/O connector |            |
| Aux110                                 | 189919-0R5 |
| Related Products                       |            |

#### Related Products

NI 5421 arbitrary waveform generators

NI PXI-5404 clock and frequency generator

NI PXI-655x digital waveform generator/analyzers

## **BUY NOW!**

For complete product specifications, pricing, and accessory information, call 800 813 3693 (U.S.) or go to **ni.com/digitizers**.

| Maximum Data Streaming Rates <sup>4</sup> |             |  |
|---|-------------|--|
| PCI/PXI                                   | PXI Express |  |
| 110 MB/s                                  | 400 MB/s    |  |

#### Accuracy

#### DC accuracy (0 V offset setting)

| Full Scale Input Range   | 50 $\Omega$ and 1 M $\Omega$                               |
|--------------------------|--|
| 200 mV, 400 mV, 1 V, 2 V | $\pm 0.65\%$ of input $\pm 1.0$ mV ( $\pm 2.0$ mV for PCI) |
| 4 V, 10 V                | ±0.65% of input ±8.0 mV                                    |
| 20 V                     | ±0.65% of input ±10.0 mV                                   |

#### Passband Flatness (referenced to 50 kHz)

|                     | Full Scale Input Range               | 50 $\Omega$ and 1 M $\Omega$                 |
|---------------------|--------------------------------------|--|
| Filters off         | 400 mV, 1 V, 2 V, 5 V,<br>10 V, 20 V | ±0.4 dB DC to 20 MHz<br>±1 dB 20 to 50 MHz   |
|                     | 200 mV                               | ±0.4 dB DC to 20 MHz<br>±1 dB 20 to 40 MHz   |
| Antialias filter on | All ranges                           | ±1.2 dB DC to 16 MHz<br>±1.6 dB 16 to 32 MHz |
| AC amplitudo aco    | (50  kHz)                            | 0 O +0 06 dB 1 MO +0 00 dB                   |

AC amplitude accuracy (50 kHz)....... 50  $\Omega$ : ±0.06 dB, 1 M $\Omega$ : ±0.09 dB Channel-to-channel crosstalk .........  $\leq$ -100 dB at 10 MHz

### **Spectral Characteristics (typical)**

# Dynamic performance (50 $\Omega$ input impedance with 10 MHz, -1 dBFS input signal, filters on)

| Full Scale Input Range | SFDR (dBc) | THD (dBc) | SNR (dB) | SINAD (dB) |
|------------------------|------------|-----------|----------|------------|
| 200 mV                 | 75         | -75       | 60       | 60         |
| 400 mV                 | 75         | -75       | 62       | 62         |
| 1 V                    | 75         | -75       | 62       | 62         |
| 2 V                    | 75         | -75       | 62       | 62         |
| 4 V                    | 65         | -63       | -        | -          |
| 10 V                   | 65         | -63       | -        | -          |

SFDR = Spurious-free dynamic range

THD = Total harmonic distortion SNR = Signal-to-noise ratio, excluding distortion (antialias filter enabled)

SINAD = Signal-to-noise and distortion (antialias filter enabled)

#### Dynamic performance (1 M $\Omega$ input impedance with 10 MHz, -1 dBFS input signal, filters on)

| Full Scale Input Range   | SFDR (dBc)   | THD (dBc)  | SNR (dB)  | SINAD (dB)   |  |
|--|--|--|---|--|--|
| 200 mV   | 70   | -68  | 60  | 59   |  |
| 400 mV   | 70   | -68  | 62  | 61   |  |
| 1 V  | 70   | -68  | 62  | 61   |  |
| 2 V  | 70   | -68  | 62  | 61   |  |
| 4 V<br>10 V  | /U   | -08  | 62  | 61   |  |
| 20 V   | 60   | -58  | _   | _  |  |
| SFDR = Spurious-free dynamic r<br>THD = Total harmonic distortion<br>SNR = Signal-to-noise ratio, exc<br>SINAD = Signal-to-noise and dis | ange<br>Iuding distortion (ar<br>stortion (antialias fil | tialias filter enable<br>ter enabled)  | d)  |  |  |
| Intermodulation distor   | tion <sup>5</sup> (IMD)                                  | <75 dB   | С   |  |  |
| Phase noise density (1   | 10 MHz input   | ) <-100 c<br><-120 c<br><-130 c  | <-100 dBc/Hz at 100 Hz,<br><-120 dBc/Hz at 1 kHz,<br><-130 dBc/Hz at 10 kHz |  |  |
| Acquisition Mo   | des  |  |   |  |  |
| Real-time sample rate  | )  | 100 MS<br>samplir  | S/s to 1.526 I<br>ng rate   | <s s<="" td=""></s>                                  |  |
| Random interleaved sa  | ampling (RIS)  | 2 GS/s<br>rate (re   | to 200 MS/s<br>petitive sign  | sampling<br>als only)                                |  |
| <b>Timebase Syste</b>  | m  |  |   |  |  |
| Timebase ontions   |  | Internal   | PXI star ext  | ernal (CLK IN)                                       |  |
| Total sample clock jitt  | ≤1 ps <sub>rm</sub>                                      | $\leq 1 \text{ ps}_{\text{rms}}$   |   |  |  |
| Internal   |  |  |   |  |  |
| Internal sample clock  | 100 MS<br>with de<br>1 ≤ n ≤<br>+25 pp                   | 100 MS/s sampling rate<br>with decimation by n where<br>$1 \le n \le 65,535$<br>+25 ppm (+0.0025%) |   |  |  |
|  |  | ±zo ppi  | 11 (±0.0023 /0  | 1  |  |
| External   |  |  |   |  |  |
| External sample clock sources<br>External sample clock range   |  | CLK IN<br>30 to 1<br>PXI Sta<br>decima<br>1 ≤ n ≤  | (SMB conner<br>05 MHz (to 8<br>r Trigger), va<br>tion by n wh<br>65,535     | ctor), PXI star<br>0 MHz using<br>riable with<br>ere |  |
| External reference clo   | ck sources   | CLK IN<br>PXI bac  | (SMB conne<br>kplane 10 M   | ctor),<br>Hz   |  |
| External reference clo   | ck range   | 1 to 20<br>increme   | MHz in 1 MI<br>ents   | Hz   |  |
| Trigger System   |  |  |   |  |  |
| Modes  |  | Edae h   | vsteresis, wi   | ndow, video  |  |
|  |  | diaital  | immediate s   | software   |  |
| Sources  |  | CH 0, C<br>PFI <0:1  | :H 1, TRIG, P)<br>1>, PXI Star, 3   | <i_trig <0:6="">,<br/>Software</i_trig>              |  |
| Slope  |  | Rising (   | or falling  |  |  |
| Video trigger  |  | Negativ<br>and SE  | ve sync of N1<br>CAM standar  | SC, PAL,<br>ds                                       |  |
| Video trigger types  |  | Any line   | e, specific line  | , specific field                                     |  |

High-frequency reject filter..... 50 kHz, software selectable Low-frequency reject filter ..... 50 kHz, software selectable

| Sensitivity          | CH 0 and CH 1: 2.5% FS up to<br>50 MHz increasing to 5% FS at<br>100 MHz <sup>-</sup> TBIG: 2.5% up to |
|----------------------|--|
|                      | 100 MHz increasing to 10%  |
|                      | at 200 MHz   |
| Level accuracy       | CH 0, CH 1, and TRIG: ±3.5% FS   |
|                      | up to 10 MHz   |
| Time resolution      | 100 ps with time-to-digital  |
|                      | converter enabled  |
| Holdoff <sup>7</sup> | $2 \mu$ to 171.79 s, software selectable   |
|                      |  |

## **External Trigger Channel (TRIG)**

| Impedance | $1~\text{M}\Omega$    22 pF |
|-----------|-----------------------------|
| Range     | ±5 V                        |
| Coupling  | AC, DC                      |

#### **Power**

| Typical |          |        |         |         |             |
|---------|----------|--------|---------|---------|-------------|
|         | +3.3 VDC | +5 VDC | +12 VDC | -12 VDC | Total Power |
| PXI     | 1.4 A    | 1.5 A  | 110 mA  | 270 mA  | 16.7 W      |
| PCI     | 1.4 A    | 2.4 A  | 110 mA  | 0 A     | 17.9 W      |
| PXIe    | 1.6 A    | 0 A    | 2.0 A   | 0A      | 29.3 W      |

#### **Environment**

| Operating temperature <sup>8</sup> | 0 to 55 °C (meets IEC-60068-2-1<br>and IEC-60068-2-2)                               |
|------------------------------------|---|
| Storage temperature                | -40 to 71 °C (meets IEC-60068-2-1 and 60068-2-2)                                    |
| Relative humidity                  | 10 to 90%, noncondensing<br>(meets 60068-2-56)                                      |
| Calibration                        | (   |
| NIST traceability                  | Factory calibrated to verify it meets NIST-traceable standards                      |
| Self-calibration                   | Gain, offset, frequency<br>response, triggering, and<br>timing for all input ranges |
| External calibration interval      | 2 years   |
| Routine calibration                | Return your device to   |
|                                    | National Instruments or ship to a qualified metrology lab                           |
|                                    |   |

<sup>1</sup>Bandwidth on 200 mV range is 80 MHz.

<sup>2</sup>NI 5122 also supports continuous acquisition.

<sup>3</sup>0 to 100% pretrigger and posttrigger data.

<sup>4</sup>Rates are for a single NI 5122 device. Dedicated per-slot bandwidth available in PXI Express enables multimodule systems to achieve higher aggregate data rates. Using a 12-drive high-performance RAID system, aggregate data rates up to 600 MB/s to disk are possible.

<sup>5</sup>Measured on ranges up to 2 V on 50  $\Omega$  input with two tones at 10.2 and 11.2 MHz, each at -7 dBFS.

<sup>6</sup>Includes effects of converter aperture and clock circuitry jitter from 100 Hz to 100 kHz. 7Time-digital-converter disabled.

 $^{8}\mathrm{O}$  to 45 °C in PXI-101x and 1000/B chassis.

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NI Factory Installation Services (FIS) is the fastest and easiest way to use your PXI or PXI/SCXI combination systems right out of the box. Trained NI technicians install the software and hardware and configure the system to your specifications. NI extends the standard warranty by one year on hardware components (controllers, chassis, modules) purchased with FIS. To use FIS, simply configure your system online with **ni.com/pxiadvisor**.

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